

ABSTRACT OF THE DISCLOSURE

An LCD panel and a method for manufacturing the same is disclosed, in which light leakage is prevented from occurring by forming a dummy pattern in an array peripheral region. The LCD panel includes a first substrate having an array region and an array peripheral region, a gate line on the first substrate, a gate insulating film on the entire surface of the first substrate including the gate line, a data line arranged to cross the gate line for defining a pixel region on the array region, a light leakage prevention film formed between the gate and/or data lines of the array peripheral region for preventing light leakage in the panel, and a TFT and a pixel electrode formed in each pixel region.